

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: §
JAMES E. GREEN §
§ Examiner: Unknown
Serial No.: Unknown §
§ Group Art Unit: Unknown
Filed: Concurrently Herewith §
§ Att'y Docket: 2008.006600 / 02-0891.00
For: METHOD AND APPARATUS FOR §
INTEGRATED CIRCUIT FAILURE §
ANALYSIS §

**ELECTION UNDER 37 C.F.R. §§ 3.71 AND 3.73
AND POWER OF ATTORNEY**

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

The undersigned, being Assignee of record of the entire interest in the above-identified application by virtue of an assignment recorded in the United States Patent and Trademark Office as set forth below, hereby elects, under 37 C.F.R. § 3.71, to prosecute the application to the exclusion of the inventors.

The Assignee hereby revokes any previous Powers of Attorney and appoints:

Michael L. Lynch, Reg. No. 30,871; Lia M. Pappas, Reg. No. 34,095; and
W. Eric Webostad, Reg. No. 35,406; of Micron Technology, Inc.; and

Danny L. Williams, Reg. No. 31,892; Terry D. Morgan, Reg. No. 31,181; J. Mike Amerson, Reg. No. 35,426; Kenneth D. Goodman, Reg. No. 30,460; Jeffrey A. Pyle, Reg. No. 34,904; Jaison C. John, Reg. No. 50,737; Ruben S. Bains, Reg. No. 46,532; Scott F. Diring, Reg. No. 35,119; Shelley P.M. Fussey, Reg. No. 39,458; Mark D. Moore, Reg. No. 42,903; Raymund F. Eich, Reg. No. 42,508; Daren C. Davis, Reg. No. 38,425; Stephanie A. Wardwell, Reg. No. 48,025; and
Mark W. Sincell, Reg. No. 52,226 of Williams, Morgan & Amerson, P.C.

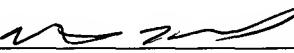
as its attorney or agent for so long as they remain with such firm, with full power of substitution and revocation, to prosecute the application, to make alterations and amendments therein, to transact all business in the Patent and Trademark Office in connection therewith, and to receive any Letters Patent, and for one year after issuance of such Letters Patent to file any request for a certificate of correction that may be deemed appropriate.

Pursuant to 37 C.F.R. § 3.73, the undersigned has reviewed the evidentiary documents, specifically the Assignment to Micron Technology, Inc., referenced below, and certifies that to the best of my knowledge and belief, title remains in the name of the Assignee.

Please direct all communications as follows:

Danny L. Williams
WILLIAMS, MORGAN & AMERSON, P.C.
10333 Richmond, Suite 1100
Houston, Texas 77042
(713) 934-4060

ASSIGNEE:
MICRON TECHNOLOGY, INC.

By: 
Michael L. Lynch
Chief Patent Counsel

Date: 7-17-03

ASSIGNMENT:

Concurrently filed
 Previously recorded

Date: _____
Reel: _____
Frame: _____

DECLARATION

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or the below named inventors are the original, first and joint inventors (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled "**METHOD AND APPARATUS FOR INTEGRATED CIRCUIT FAILURE ANALYSIS**", the Specification of which:

is attached hereto.
 was filed on as Application Serial No.

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims.

I acknowledge the duty to disclose to the Patent and Trademark Office all information known to me to be material to patentability of the subject matter claimed in this application, as "materiality" is defined in Title 37, Code of Federal Regulations, § 1.56.

I hereby claim priority benefits under Title 35, United States Code, § 119 of any foreign application(s) for patent, United States provisional application(s), or inventor's certificate listed below and have also identified below any foreign application for patent, United States provisional application, or inventor's certificate having a filing date before that of the application on which priority is claimed:

PRIORITY APPLICATION(S)			Priority Claimed
(Number)	(Country)	(Date Filed)	Yes/No

I hereby claim the benefit under Title 35, United States Code, § 120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, § 112, I acknowledge the duty to disclose all information known to me to be material to patentability of the subject matter claimed in this application, as "materiality" is defined in Title 37, Code of Federal Regulations, § 1.56, which become available between the filing date of the prior application and the national or PCT international filing date of this application:

(Application Serial No.)	(Filing Date)	(Status)

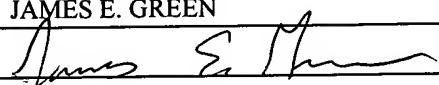
I hereby direct that all correspondence and telephone calls be addressed to **Danny L. Williams**, Williams, Morgan & Amerson, P.C., 10333 Richmond, Suite 1100, Houston, Texas 77042, (713) 934-4060.

I HEREBY DECLARE THAT ALL STATEMENTS MADE OF MY OWN KNOWLEDGE ARE TRUE AND THAT ALL STATEMENTS MADE ON INFORMATION AND BELIEF ARE BELIEVED TO BE TRUE; AND FURTHER THAT THESE STATEMENTS WERE MADE WITH THE KNOWLEDGE THAT WILLFUL FALSE STATEMENTS AND THE LIKE SO MADE ARE PUNISHABLE BY FINE OR IMPRISONMENT, OR BOTH, UNDER SECTION 1001 OF TITLE 18 OF THE UNITED STATES CODE AND THAT SUCH WILLFUL FALSE STATEMENTS MAY JEOPARDIZE THE VALIDITY OF THE APPLICATION OR ANY PATENT ISSUED THEREON.

Inventor's Full Name:

JAMES E. GREEN

Inventor's Signature:



Country of Citizenship:

USA

Date:

7-9-03

Residence Address: (street,
number, city, state, and/or
country)

7612 Cypress Lane

Nampa, ID 83687

Post Office Address:
(if different from above)
